

## CLAIMS

We claim:

1. An integrated-circuit ("IC") layout comprising:
  - a) net with routable elements;
  - b) a first set of interconnect lines for connecting the routable elements of the nets, wherein the interconnect lines have ends that are in shape of a partial non-quadrilateral polygons.
2. The IC layout of claim 1, wherein the partial polygon is a partial octagon.
3. The IC layout of claim 2, wherein the partial octagon has several sides that are equal.
4. The IC layout of claim 3, wherein the partial octagons are half octagons.
5. The IC layout of claim 1, wherein the partial polygon is a partial hexagon.
6. The IC layout of claim 5, wherein the partial hexagon has several equal sides.
7. The IC layout of claim 5, wherein the partial hexagons are half hexagons.
8. The IC layout of claim 1 further comprising a first set of vias that are in shape of the non-quadrilateral polygon.
9. The IC layout of claim 8, wherein the polygon is an octagon.

10. The IC layout of claim 9, wherein the octagon has several sides that are equal.
11. The IC layout of claim 10, wherein the partial octagon ends of the interconnect lines are half octagons.
12. The IC layout of claim 8, wherein the polygon is a hexagon.
13. The IC layout of claim 12, wherein the hexagon has several equal sides.
14. The IC layout of claim 13, wherein the partial hexagon ends of the interconnect lines are half hexagons.
15. The IC layout of claim 1 further comprising a first set of vias that are in shape of another non-quadrilateral polygon.
16. The IC layout of claim 1 further comprising a second set of interconnect lines that have end that are in shape of a partial quadrilateral polygons.
17. The IC layout of claim 16, wherein the second set of interconnect lines have partial rectangular ends.
18. The IC layout of claim 16 further comprising a second set of vias that are in shape of a quadrilateral polygon.